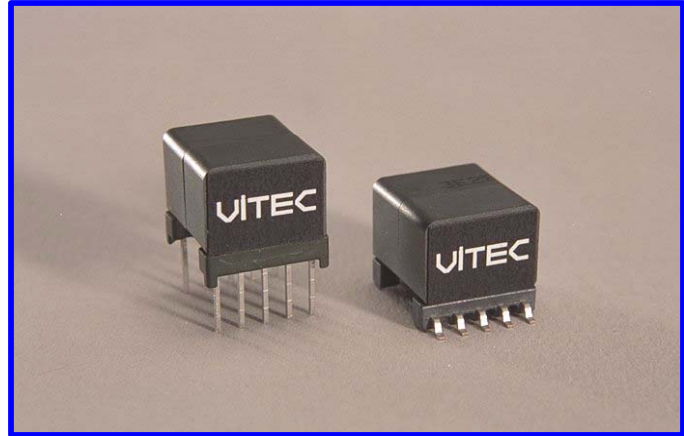


TRANSFORMERS IN SMD OR THT PACKAGES

HDSL and POTS Splitters Applications

FEATURES

- Designed for digital telecom applications.
- Product matched to leading transceiver ICs.
- Standard 10 pin SMD or THT packages.
- Designed to exceed ANSI and ETSI standards.
- Excellent total harmonic distortion.
- HDSL transformers built to TNV-3 of IEC60950/EN60950.
- Other designs available.



<p>70ZR7xxx</p> <p>Drawing NOT to Scale. Dimensions Inches/mm. +/- 0.005/0.127 Unless noted.</p>	<p>SCHEMATICS</p> <p>Line Chip</p>	<p>60ZRxxx</p> <p>Drawing NOT to Scale. Dimensions Inches/mm. +/- 0.005/0.127 Unless noted.</p>
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ELECTRICAL CHARACTERISTICS @ 25°C												
Part Number		Turns Ratio	LP ⁽¹⁾	LL ⁽²⁾	Primary DCR	Secondary DCR	Dielectric Strength	THD	Insertion Loss	Longitudinal Balance	Bias (both windings)	Schem.
SMD	THT	Ratio +/- 1%	mH +/- 10%	uH MAX	Ohms MAX	Ohms MAX	Vrms MIN	dB MIN	dB MAX	dB MIN	ADC	
Coupled Inductor for POT Low Pass Filter												
---	70ZR7008	1:1	5.50	12	5.25	5.25	850	--	--	--	0 - 0.080	A
60ZR823	70ZR7023	1:1	6.00	--	4.40	4.40	1500	--	--	--	0 - 0.060	B
60ZR824	70ZR7024	1:1	4.00	--	3.60	3.60	1500	--	--	--	0 - 0.100	B
60ZR825	70ZR7025	1:1	3.00	--	3.00	3.00	1500	--	--	--	0 - 0.100	B
HDSL Transformers												
60ZR850	70ZR7250	2:1	2.00	11	2.50	1.00	2100	70 dB @ 20KHz	0.5 dB @ 40KHz	50 dB @ 40-300KHz	--	C
60ZR872	70ZR7272	2.3:1	2.00	35	(1-4/2-5) 2.05/2.05	(6-9) 1.00	1500	-80 dB @ 20KHz	--	55 dB @ 20-560KHz	--	D
<p>Notes: ⁽¹⁾ Tested @10KHz, 0.1V ⁽²⁾ Tested @100KHz, 0.1V</p>												



T-680.3 10/14

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