VITEC

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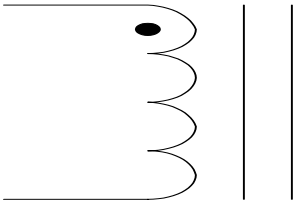
Power Inductor Design Form

Date:		Telephone No.:	
Customer Name:		Fax No.:	
Design Engineer:		E-mail Address:	
Program Name:		Type of Circuit:	



Design Inputs No.1	Inductance @ 0ADC	Inductance @ Peak I	Rated IDC	Ripple I (Pk to Pk)	Operating Frequency	DC Resistance	Maximum Temp. Rise
Inductor Inputs							

If Inductance Value Unknown, Please Complete One of The following:

Design Inputs No. 2	Maximum V in	Minimum V in	Operating Frequency	Minimum V out	Minimum I out	% Ripple Current	Maximum Temp. Rise
Buck Converter							
Note: Continuous Mode							

Design Inputs No. 3	Minimum V in	Operating Frequency	Minimum V out	Minimum I out	% Ripple Current	Maximum Temp. Rise
Boost Converter						
Note: Continuous Mode						

For all other topologies, please contact the factory.

Temperature / Cooling	Max. Ambient	Max. Operating	Cooling
Unit Measurement: °C			
Notes: 1. Maximum Operating Temperature is determined by Temperature rating of insulation 2. Cooling CFM is typically 1/3 of fan rating. Enter “0” if not forced air-cooled.			

Safety Requirements:	__ UL / CSA; __ International: _____
Maximum Desired Size:	_____(Length) x _____(Width) x _____(Height)
Mounting Style	__ Surface Mount Device; __ Through-Hole Devise